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Catalyst 9000 Switching QoS Deep Dive

Kenny Lei Technical Marketing Engineer (TME) BRKENS-2096



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	8:19 Catalyst 9000 Series Switching Family • technologies, and features in the Catalyst 9000 Switches.
	Speaker(s) Kenny Lei Cicco Systems, Inc. Technical Market > Categories Technical Level Intermediate (596)
	Tracks > Networking (220)
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Agenda

- QoS Overview
- UADP QoS
- Silicon One Q200 QoS
- Config migration examples



Overview

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Do we need QoS?

User Experience

Guaranteeing voice quality

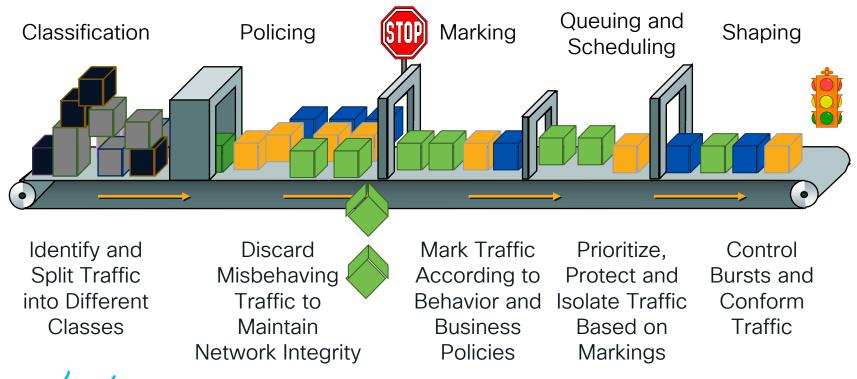
Bandwidth Savvy Business Applications protect network infrastructure to deal with abnormal events

Video Quality

de-prioritizing nonbusiness applications protecting the control planes

QoS helps define the latency priority for your traffic packets

The QoS Toolset



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QoS Terminology

Term	Explanation
Trust	Retain the packet markings as it is
Classification	Identify packet priority and place it into different classes
Marking	Change the tags (priority) on the packets
Policing	Limit the traffic to specified rate. Excess traffic can either be dropped or assigned lower priority
Shaping	Limit the traffic to specified rate. Excess traffic will be queued and buffered.
Queueing	Process the packet into separate queues
Buffering	Storage for packets to be queued

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Modular QoS CLI (MQC)

class-map What traffic do we care about?

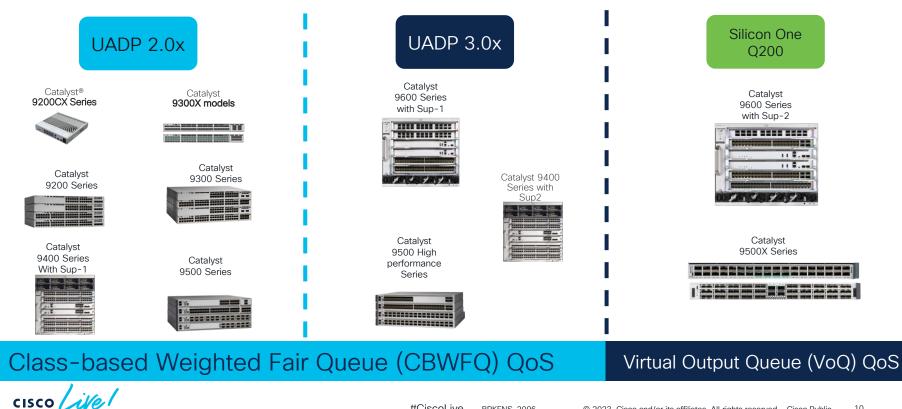
policy-map What actions do we take on the classes?

service-policy Where do we apply the policy? class-map match-any Voice
 match dscp ef
class-map match-any Video
 match dscp 34

Policy-map POLICY-QOS class Voice priority level 1 class Video set dscp 10

interface x/y
service-policy (input/output) POLICY-QOS

Catalyst 9000 family of Switches

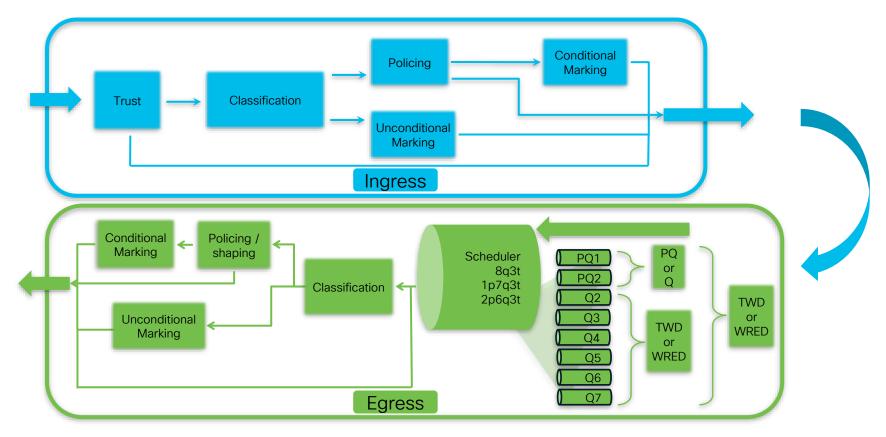


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UADP QoS (CBWFQ)

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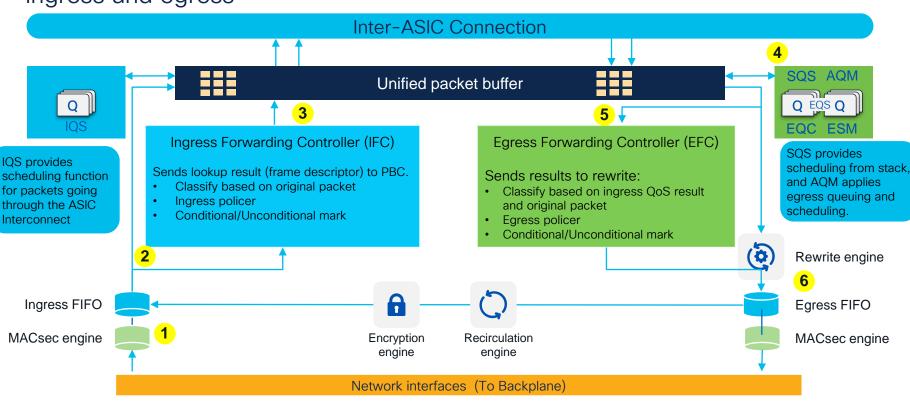
QoS Fundamental Actions in UADP



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WRED: up to 4 queues with UADP 2.0x; up to 8 queues with UADP 3.0x

UADP QoS forwarding ingress and egress



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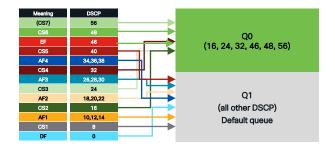
UADP forwarding

- Packet arrives at ingress port, PHY converts the signal and serializes the bits, and then it sends to network interface ports. Network interface passes packet to ingress MACsec engine. MACsec engine decrypts the packet if needed and passes unencrypted packet to ingress FIFO.
- 2. IFC snoops the packet between FIFO and PBC. IFC returns lookup result (frame descriptor) to PBC.
- 3. PBC uses the frame descriptor to determine the egress port. Egress on same ASIC, so result to moved to EQS.
- 4. EQS schedule the packet for egress process. EQS replication, scheduling, and queue management. PBC sends packet with new frame descriptor and enqueues the frame.
- 5. EFC snoops the packet between PBC and rewrite engine. EFC performs egress lookup functions to learn SRC MAC, egress SPAN, etc. and sends results to rewrite engine.
- 6. Rewrite engine rewrites packets and sends through the egress FIFO. MACsec engine encrypts packet prior to placing it on NIF.

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UADP QoS Default

- Catalyst 9000 Switches with UADP ASICs
 - QoS enabled
 - All ports trust at layer2 and layer3
 - Two queues (neither set as priority)





Classification, Marking and Policing

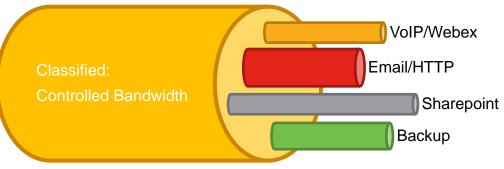
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Classification and Marking

- Identify traffic
 - Access Control Lists (ACLs)
 - DSCP
 - IP precedence
 - CoS
 - QoS Group (local with the switch)
 - EXP (MPLS)
 - Network-Based Application Recognition (NBAR) protocols *
 - VLANs
- Marking
 - Conditional or unconditional
 - Table map (default-class)
 - QoS group (local within switch)

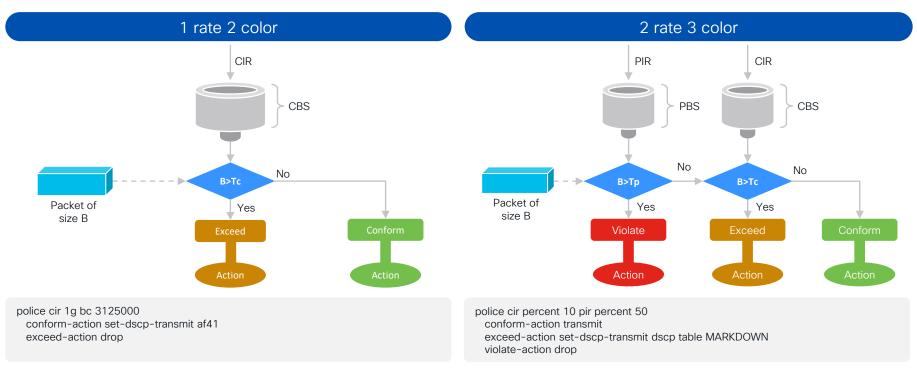




* Access platforms

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Policing – Limit the traffic



CIR – Committed Information Rate PIR – Peak Information Rate PBS- Peak Burst Size CBS - Committed Burst Size

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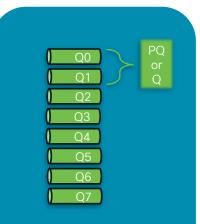
Queueing, Scheduling and Shaping





Queueing

- · Separate the traffic into the queues
- · Traffic in different queue can be treated differently
- Up-to 8 queues per interface, 2 of which can be priorityqueues (PQ).
- Both priority-queues are strict priority queues.
- Policer or a shaper on the priority queue will limit the traffic to the configured value regardless of the traffic level on other queues.



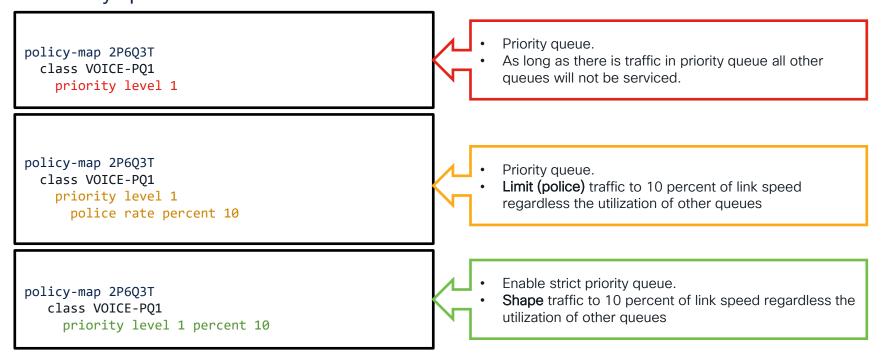
Buffer Memory

Note: Queue classification - DSCP/COS/IP Prec/QoS Group





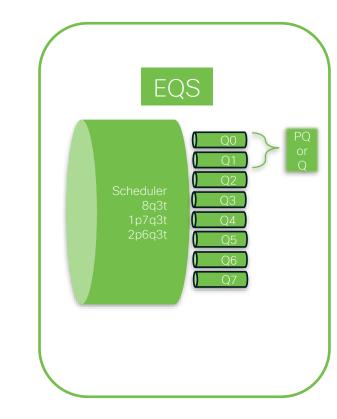
Queueing Priority queue



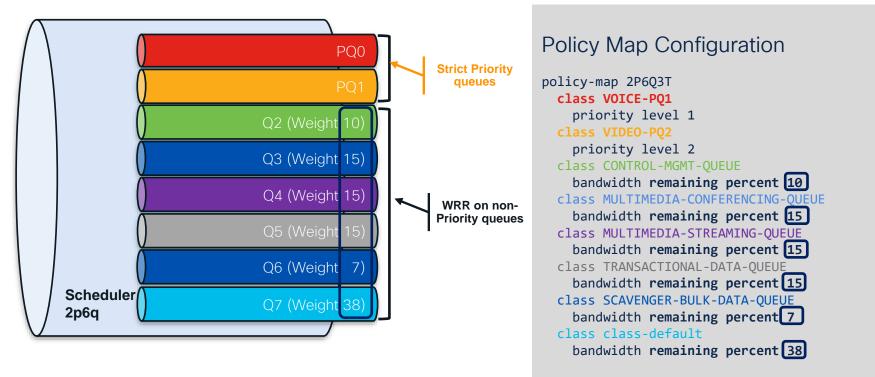
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Scheduling - UADP

- Scheduling defines the order of transmission of traffic out of the queues
- Different type of queues are served differently
 - Strict priority queues
 - Always serviced first
 - With 2 PQs, level1 over level 2
 - Normal queues
 - Served only after priority queues are empty
 - Use Weighted Round Robin (WRR) for scheduling
- WRR servers normal queue based on the weight and packet size
- Egress Queue System (EQS) is the component on the UADP ASIC responsible for the scheduling



Scheduling - Example



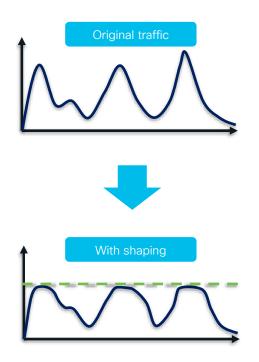
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Shaping

- Smooth out traffic peaks, microburst, with preserving traffic
- Control traffic rate to the desired value with buffering.
- Usually in the egress direction

Shaping Example

policy-map Shaper class Transactions shape average percent 30

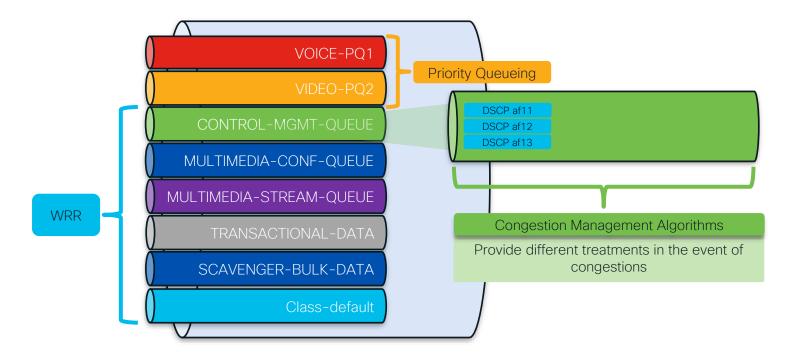


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UADP Congestion Management



Where do we need congestion management? 2P6Q3T Example





UADP - Congestion Management

Weighted Tail Drop (WTD)

- Default
- For non-priority queues
- Up to 3 thresholds per queue, one threshold per QoS tag
- Each queue need to use same QoS tag type

Weighted Random Early Detection (WRED)

- For non-priority queues
- Up to 4 queues with UADP 2.0X and up to 8 queues with UADP 3.0X
- Up to 3 threshold pairs per queue
- Each queue need to same QoS tag type

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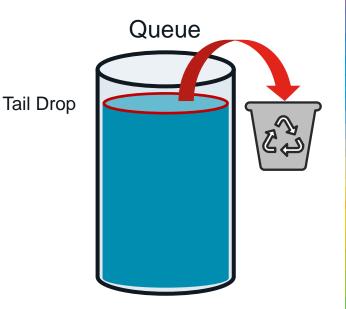
- Tail Drop (TD)
 - Drop packets at tail of the queue
 - Single threshold per queue

	Queue	
Drop		

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Tail

- Tail Drop (TD)
 - Drop packets at tail of the queue
 - Single threshold per queue

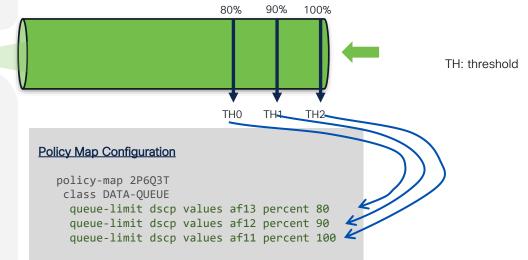


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WTD - UADP Example

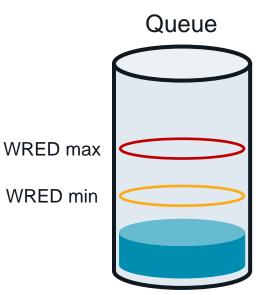


Three thresholds to conditionally drop specific traffic in the event of congestion



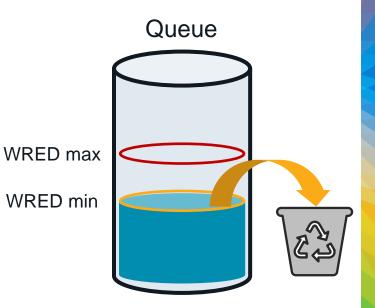


- Tail Drop (TD)
 - Drop packets at tail of the queue
 - Single threshold per queue
- Weighted Random Early Drop (WRED)
 - One or more thresholds per queue
 - Threshold associated with priority
 - Buffer usage below threshold no affect

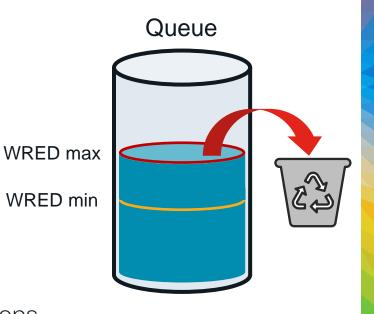




- Tail Drop (TD)
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 - Buffer usage over min threshold = random drops



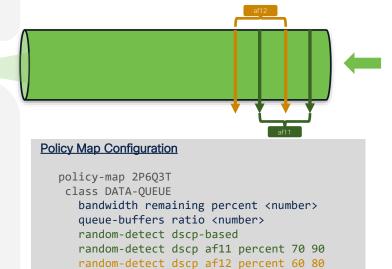
- Tail Drop (TD)
 - Drop packets at tail of the queue
 - Single threshold per queue
- Weighted Random Early Drop (WRED)
 - One or more thresholds per queue
 - Threshold associated with priority
 - Buffer usage below threshold no affect
 - Buffer usage over min threshold = random drops
 - Buffer usage over max threshold = all traffic drop



WRED – UADP Example



- Shown two pairs of WRED thresholds
- UADP supports up to 3 pairs of thresholds





Buffers

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Buffers

Allocation

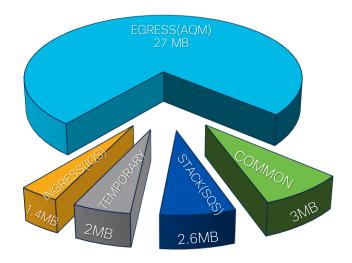
- Dedicated and shared: use dedicated first then shared
- Dynamic Threshold Scale (DTS): Algorithm to managed the shared buffer

Dedicated

- Allocated to each port on boot.
- Cannot be dynamically changed/edited

Shared

- Dynamically assigned to ports for burst absorption.
- Returned to common pool when not in use.

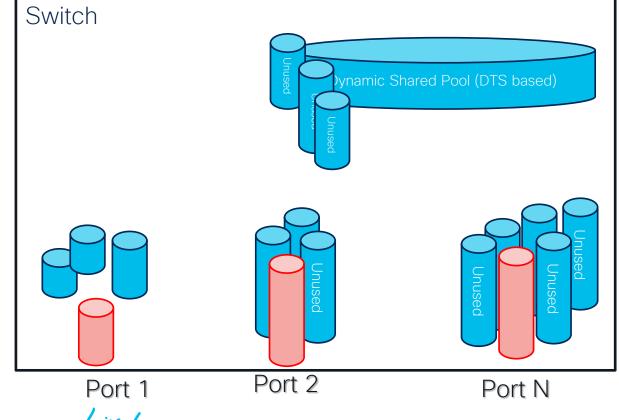




- UADP 3.0 specific
 - Buffer can be shared across two cores
 - "gos share-buffer" to enable the unified buffer

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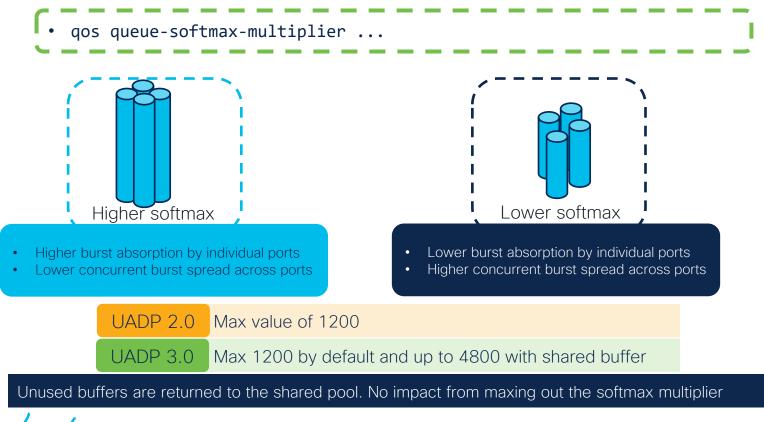
Dynamic Threshold Scale (DTS)



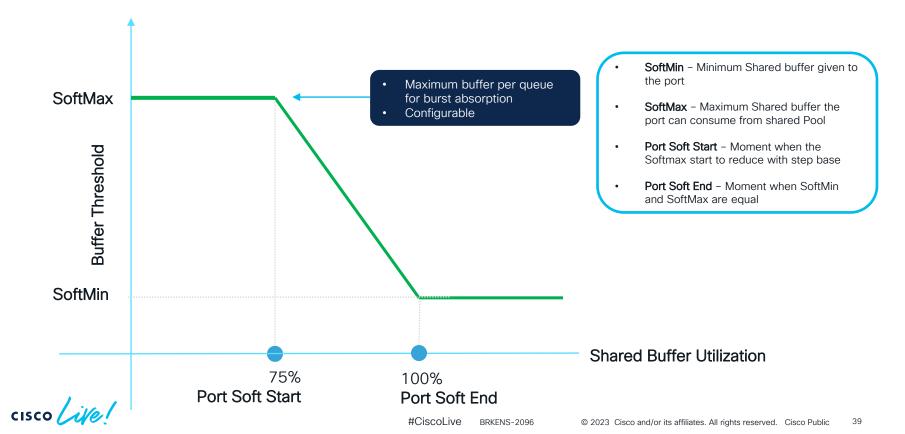
- Shared buffer is good for burst absorption.
- Dedicated buffer is good for predicated performance for each port.
- Buffer management is flexible: Dedicated plus shared.
- Configurable dedicated threshold per port/queue
- Configurable global maximum shared threshold
- Automatically adjusted depends on the available shared pool

The famous softmax multiplier

What does it do?



Dynamic Threshold Scale (DTS) buffer allocation graph



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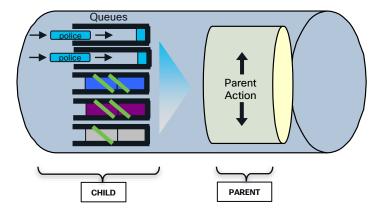
HQoS

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UADP Hierarchical QoS (HQoS)

HQoS (two-level hierarchy) allows you to perform the following functions:

- Classification
- Policing
- Shaping



Child Policy	Parent Policy
Classification + Policing	Shaping
	Marking
Classification + Marking	Policing
	Shaping

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Silicon One Q200 QoS (VoQ)



Silicon One QoS Terminology

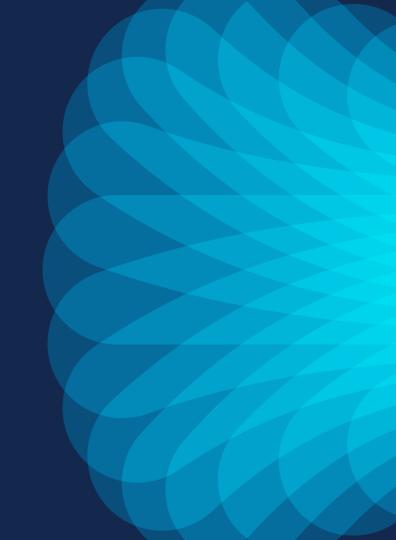
Term	Explanation
VoQ	Virtual Output Queues between Ingress and Egress
Packet Color	Used for congestion management to prioritize packets to be dropped
Traffic-Class	Internal tag used by the Silicon One ASIC to differentiate packet priority
Traffic Manager	The block in Q200 responsible for scheduling
Traffic/Transmit Scheduler	When the OQ can send traffic out to the wire
Credit Scheduler	When the VoQ can send traffic to the Output queue
SMS	Shared Memory Sub-system - Primary Buffering system
HBM	High Bandwidth Memory - Secondary deep Buffering system used during congestion

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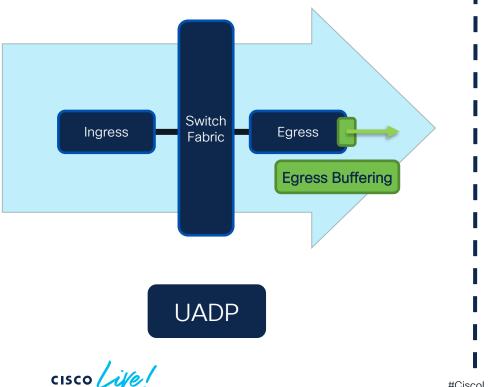
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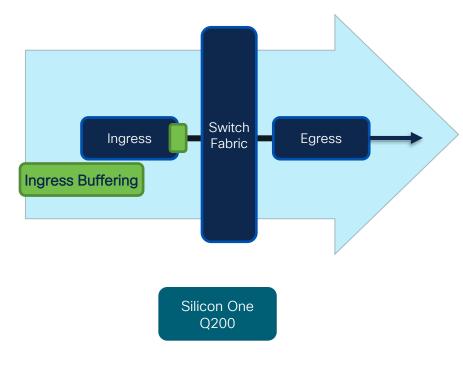
VoQ and Head of Line Blocking (HoL)

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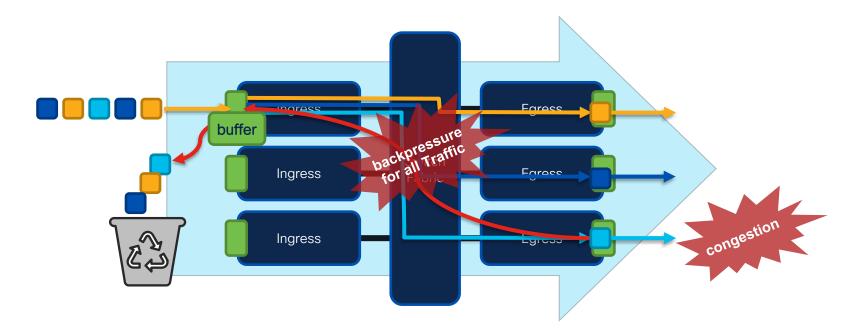


Buffer types – Silicon One vs UADP



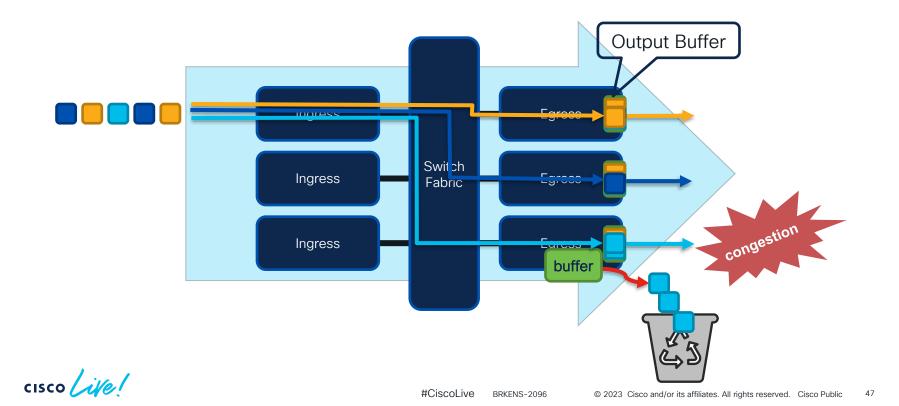


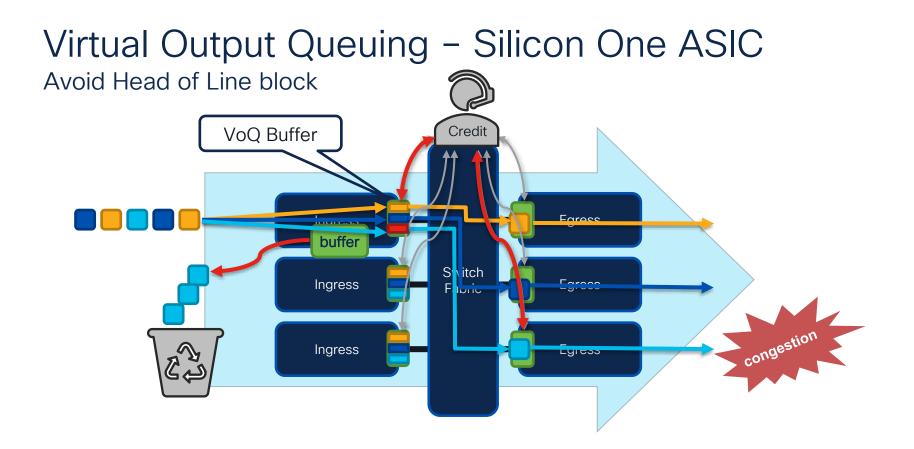
Ingress buffering – Head of Line Blocking What is the Problem?



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Egress buffering – UADP Avoid Head of Line Blocking





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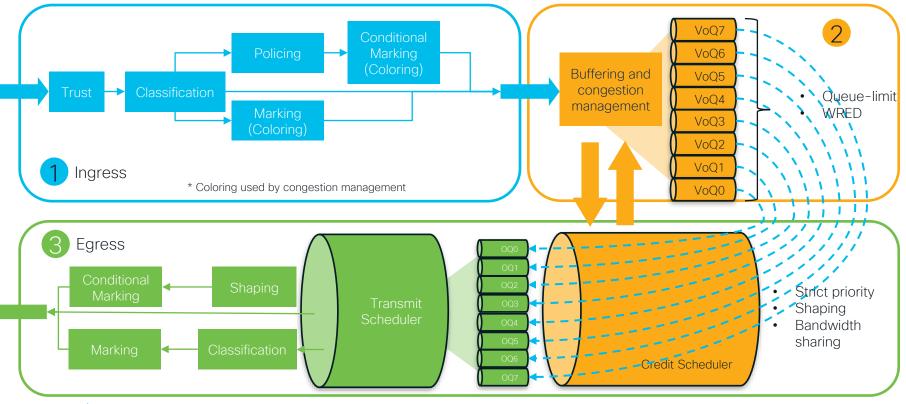
Silicon One QoS Overview





Silicon One

Features Mapping



Pererence Silicon One ASIC mapping 3 **Egress Queuing Egress Marking** Ingress Policy Policy Policy Queue Limit, Egress Ingress Egress Shaping, Classification, WRED Priority Queueing, Classification, Marking Policing, Marking Bandwidth/Sharing 2 5 VoQ 00 6 **RXPP** TM Port TXPP ____ Transmit Scheduler **RXPP** 4 ----3 VSC VSC VSC Credit Scheduler **RXPP** RXPP: RX Packet Processor Other Egress Slices TXPP: TX Packet Processor OQ: Output Queue (Egress) VoQ: Virtual Output Queue (Ingress) Ingress Egress VSC: VoQ to Scheduler Connector cisco Livel

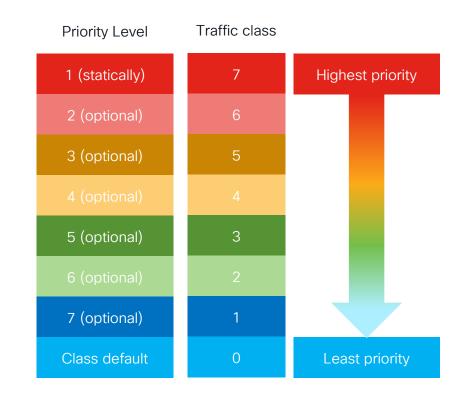


Silicon One ASIC mapping Hidden slide

- 1. Packet from the ingress interface hits the Receive Interface Group (Rx IFG). RxPP (Receive packet processor) consists of both the corresponding Interface Group as well as the Network Processing Unit (RxNPU) for the corresponding slice.
- 2. From the RxIFG, the packet descriptor is sent to corresponding VoQ (Virtual Output Queue) where it is queued and forwarded once it receives a credit.
- 3. Each VoQ connects to the scheduler via a VSC (Virtual queue scheduler) The scheduler uses a round robin (RR) algorithm to provide credits each corresponding VSC.
- 4. The credit is sent from the VSC to the corresponding VoQ. Once the VoQ receives the credit, it can forward the traffic to the egress.
- 5. The packet is sent from VoQ to corresponding OQ.
- 6. Once at the OQ, any marking operations, if any, are performed and the packet egresses out the TxNPU and TxIFG (collectively called TxPP) out of the switch.

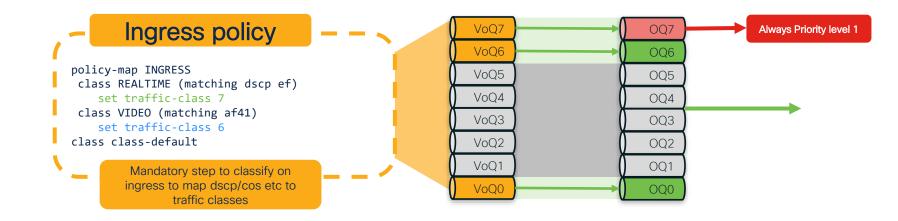
Traffic class

- S1 ASIC uses traffic classes to map traffic to different queues. "traffic-class" is local significant to the switch only
- 3-bit field => 8 values, traffic-class <0 7>
- Traffic-class 0 lowest priority (maps to classdefault); traffic-class 7 - highest priority (trafficclass 1 to 6 can be non-priority)
- Ingress policies classify packets to specific traffic classes
- Class-maps in egress queuing policy can only match traffic-class



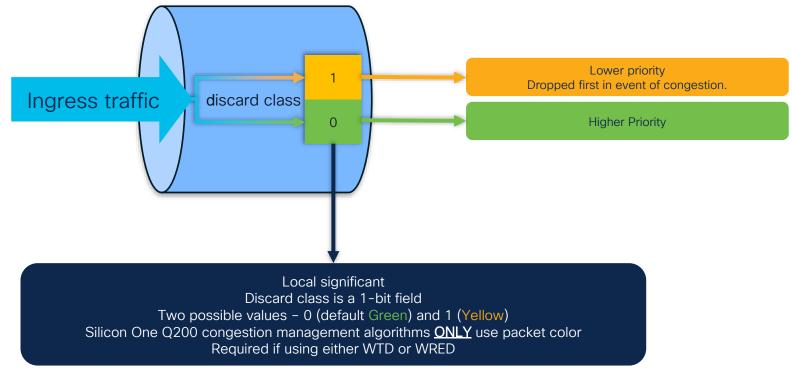


Traffic class to VoQ mapping



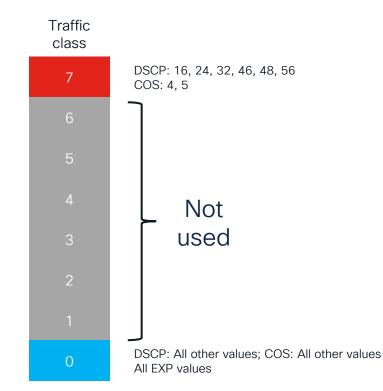
- Ingress policy determines how many VoQ are enabled.
- Each VoQ has a 1:1 mapping with corresponding OQ.

Traffic color (marking) – discard-class



Silicon One Q200 QoS Default

- QoS enabled
- All ports trust at layer2 and layer3
- Two queues (traffic-class 7 and traffic-class 0, traffic-class7 is priority level 1)



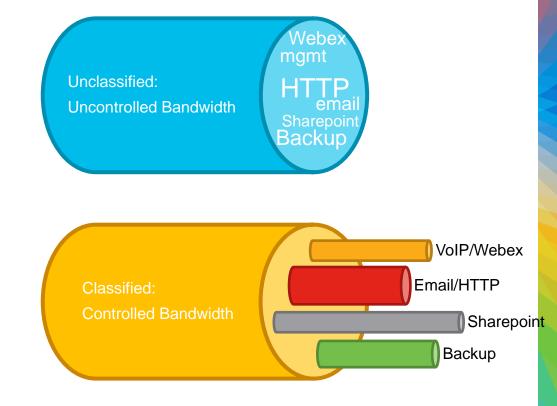


Classification, Marking and Policing

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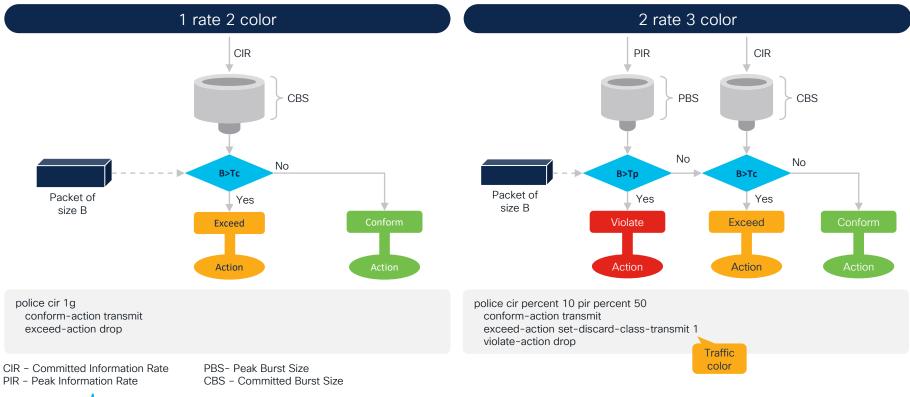
Classification and Marking

- Identify traffic
 - Access Control Lists (ACLs)
 - DSCP
 - IP precedence
 - CoS
 - QoS Group (local with the switch)
 - EXP (MPLS)
 - VLANs
- Marking (coloring)
 - Conditional or unconditional
 - Table map *
 - QoS group (local within switch)
 - Traffic-class (local within switch)
 - Traffic-color (local within switch)





Policing – Limit the traffic



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Policing and marking/coloring example

Unconditional Traffic Marking/Coloring policy-map ingress-policy

class class-5-green
set traffic-class 5
class class-5-yellow
set traffic-class 5
set discard-class 1

Different classmap Same traffic-class

1R2C Policing: policy-map test-police-1R2C class dscp1 set traffic-class 3 police rate 10g bps conform-action transmit exceed-action drop

Conditional Traffic Marking/Coloring

policy-map ingress-policy class class-5 set traffic-class 5

police rate 5g bps peak-rate 10g bps
exceed-action set-discard-class-transmit 1

2R3C Policing:

policy-map test-police-2R3C class dscp1 set traffic-class 3 police rate 10g bps peak-rate 20g bps conform-action transmit exceed-action set-discard-class-transmit 1 violate-action drop



Egress Toolset: Queueing, Shaping and Scheduling



Queueing

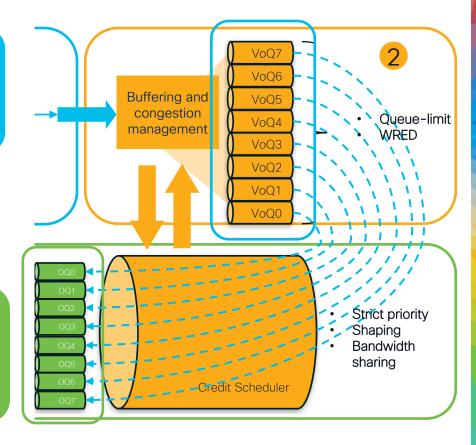
Virtual output Queue (VoQ)

- 8 VoQ on each ingress slices for each interface
- Each traffic-class maps to a VoQ (multiple traffic-classes can map to same VoQ)

• VoQ maps to output Queue.

Output Queue

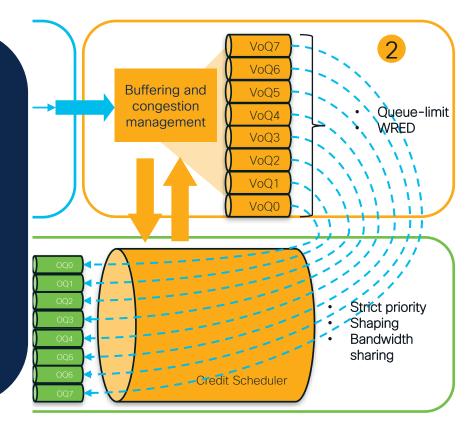
- 8 output queues (egress) for each interface
- Up to 7 strict priorities (level 1 highest)
- Traffic-class 7 is always priority level 1, priority level is optional for other trafficclasses



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Scheduling

- Packet schedule from VoQ to OQ based on a credit scheduling system
- Packets are buffered at ingress (VoQ)
- Different type of queues are served differently
 - Strict priority queues
 - Always serviced first
 - Up to 7 PQs
 - Normal queues (without priority configured)
 - Served only after priority queues are empty
 - Use Weighted Round Robin (WRR) for scheduling





Scheduling - Example

class-map match-any tc-7
match traffic-class 7
class-map match-any tc-6
match traffic-class 6

class-map match-any tc-1
 match traffic-class 1

policy-map egress-policy class tc-7 priority-level 1 class tc-6 Priority-level 2 class tc-5 bandwidth remaining ratio 1 class t-4 bandwidth remaining ratio 1 class tc-3 bandwidth remaining ratio 1 class tc-2 bandwidth remaining ratio 1 class tc-1 bandwidth remaining ratio 1 class class-default bandwidth remaining ratio 4

Map traffic to the queues

Two priority queues here

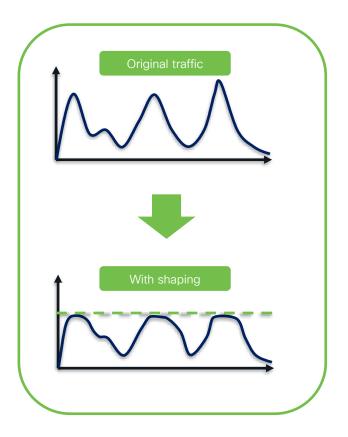
Level 1 has the absolutely priority over level 2

- Use "bandwidth remaining ratio" to assign weight
- This example gives a higher weight to class-default and same weight to rest of the queues
- Served weighted round robin around 6 queues if there isn't any traffic on the two PQs

Shaping

- Smooth out traffic peaks, microburst, with preserving traffic
- Control traffic rate to the desired value with buffering.
- Usually in the egress direction
- Can be applied on all classes, regardless of priority level.

```
Shaping Example:
policy-map type queueing egress-queueing
class tc7
    priority level 1
    shape average 1g
    class tc6
    priority level 2
    shape average 5g
...
    class class-default
    shape average 5g
```



Egress Marking



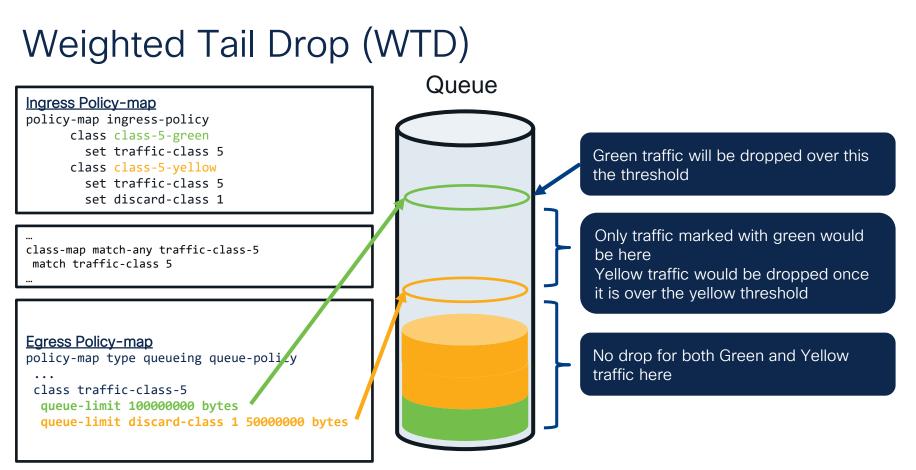
Egress Marking

- Used to change packet tags of packets egressing the switch.
- A separate policy-map apart from the queueing policy-map.
- If both queueing and marking egress policy-maps are applied, marking happens after queueing actions.
- ACL matching in egress is no supported.

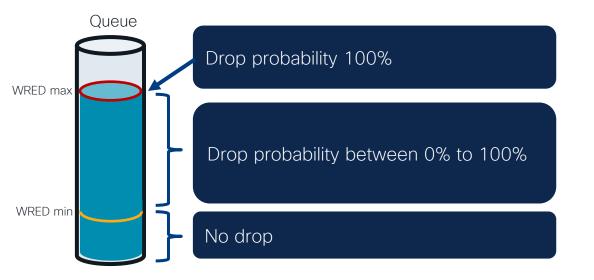
```
class-map match-any dscp-af41
match dscp af41
!
policy-map egress-map
class dscp-af41
set dscp af31
!
interface interface <#>
service-policy output egress-map
```

Congestion Management



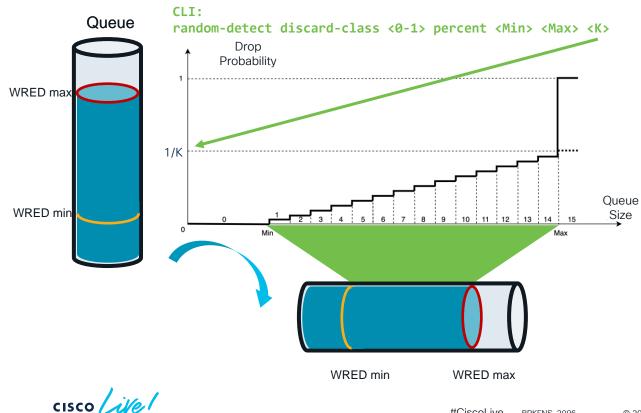


Weighted Random Early Drop (WRED)



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WRED – Drop probability



- Drop probability increases as the queue utilization increases
- Silicon One ASIC provides 16 regions (drop probabilities)
- Silicon One ASIC provides a knob to influence the drop probability

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WRED – Example

Ingress Policy-map

policy-map ingress-policy

class class-5-green
set traffic-class 5
class class-5-yellow
set traffic-class 5
set discard-class 1

class-map

class tc5 match traffic-class 5

Egress Policy-map

policy-map type queueing queue-policy

•••

class tc5 random-detect discard-class-based

random-detect discard-class 0 percent 80 90 5
random-detect discard-class 1 percent 40 70 2

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Ingress policy with marking/coloring of packets

Map the ingress class to one of the traffic-class

- Green traffic has higher Min and Max threshold comparing to yellow traffic
- Green traffic also has higher forwarding probability (lower drop probability) comparing to yellow traffic

Buffers

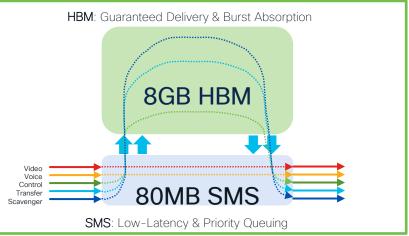
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Silicon One Buffers

2 different buffers to address 2 different requirements.

- Shared Memory Sub-system (SMS) buffers (80MB):
 - Low latency packet queueing (video/voice packets)
 - · Shallow specialized pool of buffers for faster queueing.
- High Bandwidth Memory (HBM) buffers (8GB):
 - Deep pool of on-demand buffers for guaranteed delivery.
 - Reserve to absorb occasional micro-bursts
 - · Absorb speed over-subscription from ingress to egress.





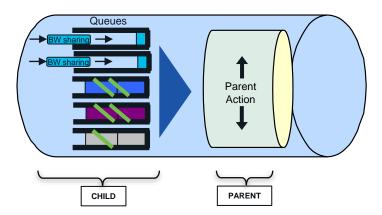
- Packet will always hit the SMS buffers first.
- SMS send the packet to HBM if additional buffers are needed.
- HBM <u>CANNOT</u> send the packet to the output queue, it has to be sent to the SMS again to be sent to the egress.

HQoS

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Silicon One Hierarchical QoS (HQoS)

HQoS (two-level hierarchy) allows a parent and child policies on an interface for greater granularity. The Cisco Silicon One supports shaping as parent action.



Child Action	Parent Action	
Bandwidth sharing + Priority	Shaping	



Silicon One vs UADP QoS



QoS tools on UADP and Silicon One ASICs 1/2

Features	UADP ASIC	Silicon One ASIC (Q200)	
Trust	Trust all ports by default	Trust all ports by default	
Classification	Based on Packet header and ACL for both ingress and egress	Based on packet header and ACL for ingress Based on packet header for egress. Must classify to traffic-classes at ingress.	
Marking	Header, Table-map, QoS-Group for ingress Header and table-map for egress	Header, Table-map, QoS-group, traffic- class, discard-class for ingress Header and table-map for egress	
Policing	Both ingress and egress	Ingress only	

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Pero

QoS tools on UADP and Silicon One ASICs 2/2

Features	UADP ASIC	Silicon One ASIC (Q200)	
Queueing	Based on header or QoS group Bandwidth and Bandwidth remaining	Based on traffic-class Bandwidth remaining	
Buffering	Dedicated and shared buffer with DTS	SMS: Low-latency & priority queueing HBM: Guaranteed Delivery & Burst Absorption	
Shaping	Egress	Egress	
Congestion Management	WTD: three thresholds per class WRED: three thresholds' pairs per class	WTD: two threshold per class WRED: two thresholds' pairs per class	

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QoS Config Migration

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Config Migration steps

Define the problem/behavior addressed with QoS.

Simply copy-pasting existing configs between platform families will always throw errors due to differences in syntax and supported actions between platforms.

2 How many times do you want to split your traffic – Upto 8 queues possible with our ASIC

Its often not as much as you think you need. Broad generalized splits often are more efficient than granular splits

Do you want multiple strict priority classes?

Know what strict means. All traffic coming into it will be serviced at the expense of other classes.

4 Define traffic shaping/policing or sharing between queues.

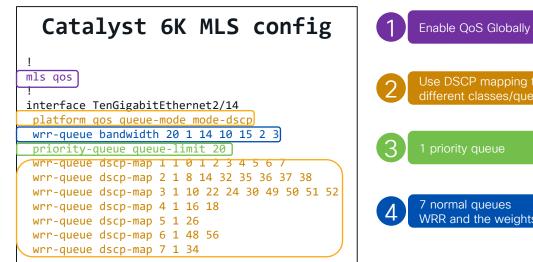
Police/shape priority queues. Use weights to control bandwidth sharing with remaining queues



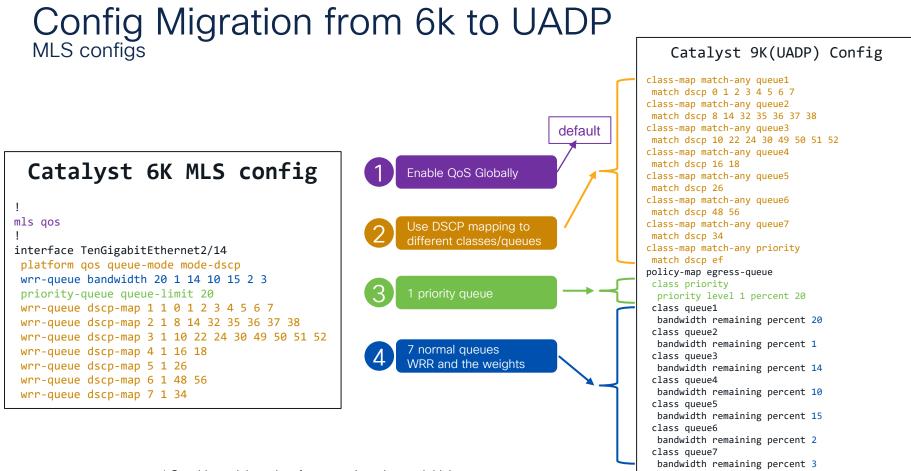
Do you want differential priority for packets within same class? - Use WTD or WRED

Advanced configuration options, not required for most use cases.

Config Migration from 6k to UADP MLS configs







* Consider weight as interface speed can be much higher now

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Config Migration from 6k to UADP

Catalyst 6K Configuration class-map type lan-queuing match-all REALTIME match dscp ef class-map type lan-queuing match-all NETWORK CONTROL match dscp cs6 cs7 class-map type lan-queuing match-all VIDEO match dscp cs3 af31 af32 af33 policy-map type lan-queuing CAMPUS EGRESS 6800 POLICY class type lan-queuing REALTIME priority level 1 class type lan-queuing NETWORK CONTROL bandwidth remaining percent 10 class type lan-queuing VIDEO bandwidth remaining percent 20 class class-default random-detect dscp-based random-detect dscp af11 percent 80 100

Interface gig1/0/1 service-policy type lan-queueing output CAMPUS_EGRESS_6800_POLICY



Use DSCP mapping to different classes/queues



3

1 priority queue

3 normal queues WRR on non-default queue WRED for class-default

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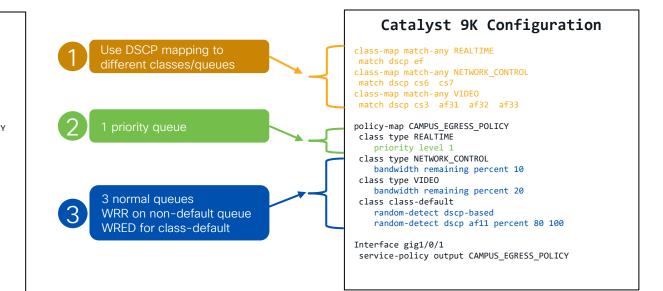
Config Migration from 6k to UADP

Catalyst 6K Configuration

class-map type lan-queuing match-all REALTIME
match dscp ef
class-map type lan-queuing match-all NETWORK_CONTROL
match dscp cs6 cs7
class-map type lan-queuing match-all VIDEO
match dscp cs3 af31 af32 af33

policy-map type lan-queuing CAMPUS_EGRESS_6800_POLICY
class type lan-queuing REALTIME
 priority level 1
class type lan-queuing NETWORK_CONTROL
 bandwidth remaining percent 10
class type lan-queuing VIDE0
 bandwidth remaining percent 20
class class-default
 random-detect dscp-based
 random-detect dscp af11 percent 80 100

Interface gig1/0/1
service-policy type lan-queueing output
CAMPUS_EGRESS_6800_POLICY



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Migration from Catalyst 6K to Silicon One Q200

Catalyst 6K Configuration

```
class-map type lan-queuing match-all REALTIME
match dscp ef
class-map type lan-queuing match-all NETWORK_CONTROL
match dscp cs6 cs7
class-map type lan-queuing match-all VIDE0
match dscp cs3 af31 af32 af33
...
policy-map type lan-queuing CAMPUS_EGRESS_6800_POLICY
class type lan-queuing REALTIME
    priority level 1
class type lan-queuing NETWORK_CONTROL
    bandwidth remaining percent 10
class type lan-queuing VIDE0
```

```
bandwidth remaining percent 20
class class-default
```

```
random-detect dscp-based
```

```
random-detect dscp af11 percent 80 100
```

Classified Based on DSCP value
 4 classes (3 defined + default)

- 3. 4 queues
- 4. 1 priority queue
- 5. Scheduling is WRR with "bandwidth remaining"
- 6. Congestion management is WRED with the default class

Config Migration from Catalyst 6K to Silicon One Q200

Catalyst 6K Configuration

```
class-map type lan-queuing match-all REALTIME
match dscp ef
class-map type lan-queuing match-all NETWORK_CONTROL
match dscp cs6 cs7
class-map type lan-queuing match-all VIDE0
match dscp cs3 af31 af32 af33
...
policy-map type lan-queuing CAMPUS_EGRESS_6800_POLICY
class type lan-queuing REALTIME
    priority level 1
class type lan-queuing NETWORK_CONTROL
    bandwidth remaining percent 10
class type lan-queuing VIDE0
    bandwidth remaining percent 20
class class-default
    random-detect dscp-based
```

random-detect dscp af11 percent 80 100

- 1. Classified Based on DSCP value
- 2. 4 classes (3 defined + default)
- 3. 4 queues (traffic-class), traffic-7 is priority level 1

Apply policy on the ingress interface

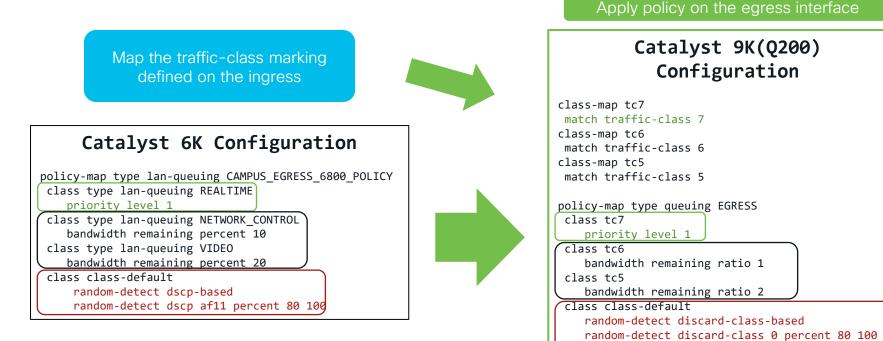
Catalyst 9K(Q200) Configuration

class-map match-all REALTIME
match dscp ef
class-map match-all NETWORK_CONTROL
match dscp cs6 cs7
class-map match-all VIDE0
match dscp cs3 af31 af32 af33
class-map match-all default-green
match dscp af11

```
policy-map INGRESS
class REALTIME
set traffic-class 7
class NETWORK_CONTROL
set traffic-class 6
class VIDEO
set traffic-class 5
class default-green
set traffic-class 0
class class-default
set discard-class 1
```

Note: class-default is always assigned with traffic-class 0

Config Migration from Catalyst 6K to Silicon One Q200



- 3. 1 priority queue
- 4. Scheduling is WRR with "bandwidth remaining"
- 5. Congestion management is WRED with the default class

88

random-detect discard-class 1 percent 40 100

Summary

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Do we need QoS?

User Experience

Guaranteeing voice quality

Bandwidth Savvy Business Applications protect network infrastructure to deal with abnormal events

Video Quality

de-prioritizing nonbusiness applications protecting the control planes

QoS helps define the latency priority for your traffic packets

Catalyst 9K Switching sessions @ CL23, US

Session Title	Session ID	Session Type
Catalyst 9000 Switching Family Architecture	TECARC-2446	Sun, 9:00am - 1:00pm
The Catalyst 9000 Switch Family - Access	BRKARC-2098	Wed, 1:00pm - 2:30pm
The Catalyst 9000 Switch Family - Core and Distro	BRKARC-2099	Tue, 4:00pm - 5:00pm
Catalyst 9000 SiliconOne and IOSXE Architecture & Innovations	BRKARC-2092	Wed, 2:30pm - 3:30pm
123 - Enterprise Campus Wired Design Fundamentals	BRKENS-1501	Mon, 8:00am - 9:00am
Designing Highly Available Networks using Catalyst 9000 Switches	BRKENS-2095	Tue, 4:00pm - 5:00pm
Designing High availability for your Wired and Wireless Access Networks	TECENS-2001	Sun, 9:00am - 1:00pm
Catalyst 9000 Switching QoS Deep Dive	BRKENS-2096	Mon, 1:00pm – 2:30pm
Catalyst 9000 Family Software Innovations and Solutions	TECENS-2618	Sun, 2:00pm - 6:00pm
Service Assurance with ThousandEyes on Catalyst 9000	BRKENS-1095	Thu, 11:00am - 12:00pm
Catalyst Powered Smart Buildings - Beyond PoE Connectivity	BRKENS-2091	Mon, 3:00pm – 4:30pm
Enabling Cloud Services at the Edge with App Hosting on Catalyst 9000	BRKENS-1090	Mon, 2:30pm – 3:30pm
BGP EVPN in Enterprise Campus using Catalyst 9000 Switches	BRKENS-2092	Tue, 1:00pm - 2:30pm
Building Time Sensitive Networks with Catalyst 9000 Switching Platforms	BRKENS-2098	Wed, 4:00pm - 5:00pm
The Industry's only Flood-Free mDNS Experience in the Enterprise Campus, Powered by Catalyst 9000	BRKENS-2097	Thu, 8:00am - 9:00am
Catalyst 9000 Switches and Cisco DNA Advantage	BRKENS-1093	Mon, 9:30am - 10:30am
Infrastructure as Code and the Cisco Catalyst 9000 Virtual Switch	BRKDEV-2467	Thu, 8:00am - 9:00am

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 Meet the Engineer meeting
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Thank you



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How:

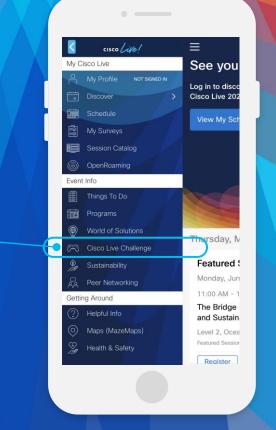


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- Open the Cisco Events App.
- Click on 'Cisco Live Challenge' in the side menu.
- Click on View Your Badges at the top.







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Let's go

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